

Serial Optimization and Vectorization Lab

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Setup

Open up a terminal

Run these commands in the terminal

- `ssh username@stampede.tacc.utexas.edu`
- `idev -m 60` (this puts you on a compute node for 60 mn)
- `source ~train00/SSASC_sourceme`
- `tar xvf ~train00/SS_ASC2016_opt_lab.tar`
- `cd SS_ASC2016_opt_lab`

Exercise 1: Optimization Levels and Performance

We are going to compare the run times of a program compiled with gcc and icc at different levels of optimization.

Setup - Perform these steps from the command line

- 1 Inspect auto.c. It times the execution of loops of different floating point operations
- 2 module swap intel gcc
- 3

```
gcc -O0 auto.c -o gauto_00 -lm  
gcc -O1 auto.c -o gauto_01 -lm  
gcc -O2 auto.c -o gauto_02 -lm  
gcc -O3 auto.c -o gauto_03 -lm
```
- 4 module swap gcc intel
- 5

```
icc -O0 auto.c -o iauto_00 -lm  
icc -O1 auto.c -o iauto_01 -lm  
icc -O2 auto.c -o iauto_02 -lm  
icc -O3 auto.c -o iauto_03 -lm
```

Exercise 1: Optimization Levels and Performance

We are going to compare the run times of a program compiled with gcc and icc at different levels of optimization.

Run these 8 binaries and record execution times for each operation (multiplication/division/sqrt)

- ❶ module swap intel gcc
- ❷ ./gauto_00
./gauto_01
./gauto_02
./gauto_03
- ❸ module swap gcc intel
- ❹ ./iauto_00
./iauto_01
./iauto_02
./iauto_03
- ❺ Which combination is the fastest? Does gcc or icc perform best?

Exercise 2: To Block or Not to Block

- Compare the run times of two matrix transposition programs
- One version will have manual loop-blocking and one will not
- 01 optimization flag has NO compiler performed blocking

Setup - Perform these steps from the command line

- 1 Inspect `cache_blocking.c`. It performs transposition w/ manual blocking and w/o
- 2 `icc -O1 cache_blocking.c -o nonblocked` (This produces an unblocked executable)
- 3 `icc -O1 cache_blocking.c -DBLOCK -o blocked` (This produces a manually blocked executable)
- 4 Run the following and note the "Time for transposition" reported
`./nonblocked`
`./blocked 32`
`./blocked 256`
`./blocked 512`

Exercise 2: To Block or Not to Block

- Compare the run times of two matrix transposition programs
- One version will have manual loop-blocking and one will not
- 02 optimization flag HAS compiler performed blocking

Setup - Perform these steps from the command line

- 1 `icc -O2 cache_blocking.c -o nonblocked_opt` (This produces a compiler blocked executable)
- 2 `icc -O2 cache_blocking.c -DBLOCK -o blocked_opt` (This produces a manual and compiler blocked executable)
- 3 Run the following and note the "Time for transposition" reported
`./nonblocked_opt`
`./blocked_opt 32`
`./blocked_opt 256`
`./blocked_opt 512`
- 4 Who is better at loop-blocking: the compiler or us?

Exercise 3: Use -xhost: Why Vectorization Matters

Setup - Perform these steps

- 1 Inspect `vector.c`. It initializes a matrix B and vector c and performs a matrix-vector multiply
- 2 `icc vector.c -no-vec -o no-vectorization` (generates un-vectorized code)
- 3 `icc vector.c -o sse2-vectorization` (generates SSE2 vectorized code (128-bit width))
- 4 `icc vector.c -xhost -o avx-vectorization` (generates AVX vectorized code (256-bit width))
- 5 Compare the run times and flops/cycle for each executable
- 6 How many doubles fit in 256-bits? Is this even possible? (Hint: SNB cores have 2 vector units)
- 7 feel free to repeat above steps w/ `-opt-report` flag and inspect generated optimization report